

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
DU, YANG, et al.

Application No.: 10/728621

Filed: 12-05-2003

Docket No.: SC13135TC

§ Patent No.: 6969656 B2
§
§ Issue Date: 11-29-2005
§
§ Examiner: H. Jey Tsai
§
§ Group Art Unit: 2812
§
§

Title: METHOD AND CIRCUIT FOR MULTIPLYING SIGNALS WITH A TRANSISTOR
HAVING MORE THAN ONE INDEPENDENT GATE STRUCTURE

Certificate of Submission

I hereby certify that this correspondence is being submitted to the USPTO,
Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal
Service with sufficient postage as first class mail.
- ☐ Facsimile transmitted in accordance with C.F.R. §1.6(d).
- ☒ Submitted electronically via EFS in accordance with "Legal Framework for
EFS Web".

February 12, 2008

Date of Submission

/Teresa Shephard/

Signature

Teresa Shephard

Printed Name of Person Signing Certificate

Commissioner for Patents
Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent.
Please enter these corrections. Since the errors appear to be on the part of the United
States Patent Office, there should be no charge.

2-8-08
Date

Respectfully submitted,



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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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PATENT NO.: 6969656 B2
APPLICATION NO: 10/728621
DATE: 12-05-2003
FIRST NAMED INVENTOR: DU, YANG

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 14, Line 23, Claim No. 2:

Change "gate material miming generally" to —gate material running generally—

In Column 14, Line 54, Claim No. 8:

Change "Conning the at least" to—forming the at least--

MAILING ADDRESS OF SENDER (Please do not use customer number below)

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